



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants: T.-Y. Chang et al. Attorney Docket No.: SIPT121256
Application No.: 10/601,709 Group Art Unit: ---
Filed: June 19, 2002 Examiner: ---
Title: INTEGRATED CIRCUIT DEVICE WITH A BUILT-IN DETECTING
CIRCUIT FOR DETECTING MAXIMUM MEMORY ACCESS TIME OF
AN EMBEDDED MEMORY

INFORMATION DISCLOSURE STATEMENT

TO THE COMMISSIONER FOR PATENTS:

Applicants are aware of the information listed in the attached form that may be material to the prosecution of the above-identified patent application.

1. X Copies of the listed patents, publications, and other information are enclosed for the Examiner's use.
2. X Pursuant to 37 C.F.R. § 1.97(b), this Information Disclosure Statement is being filed within three months of the filing date of the national application (other than a CPA), within three months of the date of entry of the national stage as set forth in 37 C.F.R. § 1.491 in an international application, before the mailing date of a first Office Action on the merits, or before the mailing date of a first Office Action after the filing of an RCE.

///

///

///

LAW OFFICES OF
CHRISTENSEN O'CONNOR JOHNSON KINDNESS^{PLLC}
1420 Fifth Avenue
Suite 2800
Seattle, Washington 98101
206.682.8100

3. X The Commissioner is hereby authorized to charge any fees under 37 C.F.R. §§ 1.16, 1.17 and 1.18 which may be required during the entire pendency of the application, or credit any overpayment, to Deposit Account No. 03-1740. This authorization also hereby includes a request for any extensions of time of the appropriate length required upon the filing of any reply during the entire prosecution of this application. A copy of this document is enclosed.

Respectfully submitted,

CHRISTENSEN O'CONNOR
JOHNSON KINDNESS^{PLLC}

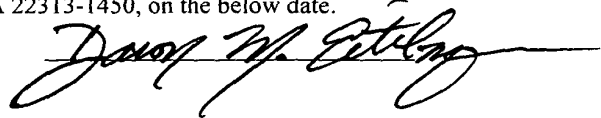


Shoko I. Leek
Registration No. 43,746
Direct Dial No. 206.695.1780

I hereby certify that this correspondence is being deposited with the U.S. Postal Service in a sealed envelope as first class mail with postage thereon fully prepaid and addressed to Mail Stop Patent Application, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on the below date.

Date:
SIL:DDP

August 29, 2003



LAW OFFICES OF
CHRISTENSEN O'CONNOR JOHNSON KINDNESS^{PLLC}
1420 Fifth Avenue
Suite 2800
Seattle, Washington 98101
206.682.8100



INFORMATION SUBMITTED BY APPLICANT(S) THAT MAY BE MATERIAL TO THE
PROSECUTION OF THE SUBJECT APPLICATION

Applicant: T.-Y. Chang et al. Attorney Docket No. SIPT121256
Application No.: 10/601,709 Group Art Unit: ---
Filed: June 19, 2003 Examiner: ---
Title: INTEGRATED CIRCUIT DEVICE WITH A BUILT-IN DETECTING
CIRCUIT FOR DETECTING MAXIMUM MEMORY ACCESS TIME OF AN
EMBEDDED MEMORY

U.S. PATENT DOCUMENTS

*Examiner Initials	Cite No.	Document No.	Kind Code	Date (mm/dd/yyyy)	Name
	U1	6,424,583		07/23/2002	Sung et al.

FOREIGN PATENT DOCUMENTS

*Examiner Initial	Cite No.	Document No.	Kind Code	Publication Date (mm/dd/yyyy)	Country	English Abstract Provided	Translation Provided
None.							

OTHER INFORMATION

(Including Author, Title, Date, Pertinent Pages, Etc.)

*Examiner Initial	Cite No.	
	O1	Sung and Wu, <i>A Method of Embedded Memory Access Time Measurement</i> , IEEE, 462-465 (2001)
	O2	Lee, Hsiao and Chang, <i>An Access Timing Measurement Unit of Embedded Memory</i> , IEEE, 104-109 (2002)

Examiner _____ Date Considered _____

*Examiner: Initial if reference considered, whether or not citation is in conformance with M.P.E.P. § 609; draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.
SIL:DDP

LAW OFFICES OF
CHRISTENSEN O'CONNOR JOHNSON KINDNESS^{PLLC}
1420 Fifth Avenue
Suite 2800
Seattle, Washington 98101
206.682.8100